

**IN THE ABSTRACT**

Please replace the Abstract of the Disclosure with the following:

-- A delay time adjusting circuit ~~adjusts a delay time of an input signal so that a~~  
~~phase of the input signal and a phase of an output signal match each other. The circuit~~  
~~comprises first dividing means for dividing a frequency of the input signal by a first~~  
~~frequency division rate; delaying means for delaying the input signal by a predetermined~~  
~~time; second dividing means for dividing for dividing a frequency of the input signal~~  
~~delayed by the delaying means by a second frequency division rate; comparing means~~  
~~for comparing a phase of a signal generated by the first dividing means and a phase of~~  
~~a signal generated by the second dividing means; and adjusting means for adjusting the~~  
~~predetermined time according to a comparison result obtained by the comparing means~~  
receives an input signal and outputs an output signal by adjusting a delay time of the  
input signal. The delay time adjusting circuit is provided with a variable delay circuit  
including a delay chain in which delay units are coupled in series for delaying the input  
signal by selecting a number of delay units to output the output signal, a divider for  
dividing a frequency of the output signal by a division rate, a phase comparison circuit  
for comparing phases of the input signal and a signal from the divider, and a delay  
adjusting circuit. The delay adjusting circuit selects the number of delay units in  
response to a comparison result in the phase comparison circuit. --